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2814

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PTO/SB/21 (6-98)  
Approved for use through 09/30/2000. OMB 0651-0031  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/881,408
	<b>Filing Date</b>	June 13, 2001
	<b>First Named Inventor</b>	Kie Y. Ahn
	<b>Group Art Unit</b>	2814
	<b>Examiner Name</b>	T. Le
<b>Total Number of Pages in This Submission</b>		<b>Attorney Docket Number</b> MI22-1534

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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	The Commissioner is hereby authorized to charge any additional fees required under 37 C.F.R. Section 1.16 and Section 1.17 and credit any overpayments to account no. 23-0925.	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
<b>Firm or Individual name</b>	James E. Lake Reg. No. 44,854; Wells St. John P.S.
<b>Signature</b>	
<b>Date</b>	28 May 2002

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App. No. 09/881,408

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/881,408  
Filing Date ..... June 13, 2001  
Inventor ..... Kie Y. Ahn, et al  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2814  
Examiner ..... T. Le  
Attorney's Docket No. .... MI22-1534  
Title: A Dielectric Layer Forming Method and Devices Formed Therewith

**RESPONSE TO FEBRUARY 26, 2002 OFFICE ACTION**

To: Box NON FEE AMENDMENT  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
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**AMENDMENTS**

**In the Specification**

Please replace paragraph 35 beginning at page 19 with the following clean replacement paragraph in accordance with 37 C.F.R. § 1.121(b)(1)(ii):

a' --Referring to Fig. 3, an MOS transistor 14 formed in accordance with embodiments of the present invention is depicted. Gate dielectric 60 is disposed over a portion of